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FACSIMILE COVER SHEET**TO: EXAMINER JENNIFER KENNEDY, GROUP ART UNIT 2812, U.S.P.T.O.****FAX NO.: 703-308-7724****PHONE NO.: 703-308-6171****FROM: NELSON H. SHAPIRO****PHONE NO.: 703-610-8687****DATE: NOVEMBER 12, 2002****TIME:****FILE NO.: APPLN. NO.: 09/989,061****PAGES (INCLUDING COVER SHEET) 3****DOCKET NO.: XA-9574****MESSAGE****FAX RECEIVED**

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XA-9574
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Hideki YASUOKA et al.

Serial No.: 09/989,061

Group Art Unit: 2812

Filed: November 21, 2001

Examiner: J. Kennedy

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND METHOD
OF MANUFACTURING THE SAME

* * *

RESPONSECommissioner for Patents
Washington, D.C. 20231

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Sir:

Responding to the Office Action mailed November 1,
2002, Applicants elect the first embodiment represented by
Figures 1-22, upon which Claims 1-7, 9-21, 23-27 and 59-60
are readable.

Applicants reserve the right to file one or more
divisional patent applications with regard to the non-
elected species.

The Commissioner is hereby authorized to charge to
Deposit Account No. 50-1165 any fees under 37 C.F.R. §§1.16
and 1.17 which may be required by this paper, and to credit
any overpayment to that Account. If any extension of time

48
11-14-02
Jenn

is required in connection with the filing of this paper and
has not been requested separately, then such extension is
hereby requested.

Respectfully submitted,

By: Nelson H. Shapiro

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Reg. No. 17,095

NHS:sjk

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November 12, 2002

CERTIFICATION OF FACSIMILE TRANSMISSION

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